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STATEMENT UNDER 37 CFR 3.73(b)
Applicant/Patent Owner: Hans Jurgen Mattausch
Application No./Patent No.: 6,874,068 Filed/Issue Date: March 29, 2005
Entitled: SHARED MEMORY
Intellectual Ventures Fund 35 LLC (Name of Assignee) , a Limited Liability Company (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)
states that it is:
1. X the assignee of the entire right, title, and interest; or
2. an assignee of less than the entire right, title and interest.
(The extent (by percentage) of its ownership interest is%)
in the patent application/patent identified above by virtue of either:
A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel
OR
B. X A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows: 1. From: Inventor To: Hiroshima University The document was recorded in the United States Patent and Trademark Office at
Reel <u>010981</u> , Frame <u>0412</u> , or for which a copy thereof is attached.
2. From: Hiroshima University To: Intellectual Ventures Fund 35 LLC
The document was recorded in the United States Patent and Trademark Office at
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3. From: To:
The document was recorded in the United States Patent and Trademark Office at
Reel, Frame, or for which a copy thereof is attached.
Additional documents in the chain of title are listed on a supplemental sheet.
As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.
[NOTE: A separate copy (<i>i.e.</i> , a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee,
10/12/08
Signature Date
Chun M. Ng (206) 359-8000
Printed or Typed Name Telephone Number
Attorney for Assignee
Title

EXHIBIT B ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Hiroshima University, a Japanese educational institution, with an office at 1-3-2 Kagamiyama, Higashi-Hiroshima City, JAPAN 739-8511, ("Assignor"), does hereby sell, assign, transfer, and convey unto Intellectual Ventures Fund 35 LLC, a Nevada limited liability company, with an office at 7251 W Lake Mead Blvd, Ste 300, Las Vegas, NV 89128 ("Assignee"), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the "Patent Rights"):

- (a) the provisional patent applications, patent applications and patents listed in the table below (the "Patents");
- (b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, (ii) for which any of the Patents directly or indirectly forms a basis for priority, and/or (iii) that were co-owned applications that directly or indirectly incorporate by reference the Patents;
- (c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);
- (d) all foreign patents, patent applications, and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;
- (e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents below and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;
- (f) all inventions, invention disclosures, and discoveries described in any item in any of the foregoing categories (a) through (e) and all other rights arising out of such inventions, invention disclosures, and discoveries;
- (g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;
- (h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for
 - (i) damages,
 - (ii) injunctive relief, and
 - (iii) any other remedies of any kind

for past, current, and future infringement; and

(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
3177637 (09-022805)	JP	4/13/2007 (2/5/1997)	Pulse width modulation operation circuit
			Iwata Atsushi; Nagata Makoto

5,889,424	US	3/30/1999	Pulse width modulation operation circuit
(09/018,836)		(2/4/1998)	r disc width modulation operation chean
			Iwata Atsushi; Nagata Makoto
2879670	JP	1/29/1999	Apparatus for processing two-dimensional
(09-078025)		(3/28/1997)	information
			Iwata Atsushi; Nagata Makoto
6,088,490	US	7/11/2000	Apparatus for processing two-dimensional
(09/047,378)		(3/25/1998)	information
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3177636	JP	4/13/2001	Pulse modulation operation circuit
(09-022804)		(2/5/1997)	Towns As all No. 1985
6,157,672	US	12/5/2000	Iwata Atsushi; Nagata Makoto Pulse modulation operation circuit
(09/018,837)	03	(2/4/1998)	ruise modulation operation circuit
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6,845,429	US	1/18/2005	Multi-port cache memory
(09/919,859)		(8/2/2001)	
			Hans Jurgen Mattaucsh; Kishi Koji; Omori Nobuhiko
01118905.7	EP	8/3/2001	Multi-port cache memory
			Hans Jurgen Mattaucsh; Kishi Koji; Omori
			Nobuhiko
90119391	TW	8/8/2001	Multi-port cache memory
			Hans Jurgen Mattaucsh; Kishi Koji; Omori Nobuhiko
10-2001-0048243	KR	8/10/2001	Multi-port cache memory
			Hans Jurgen Mattaucsh; Kishi Koji; Omori Nobuhiko
6,516,392	US	2/4/2003	Address and data transfer circuit
(09/533,336)		(3/23/2000)	
			Hans Jurgen Mattaucsh
60003482	DE	(2/24/2000)	Address and data transfer circuit
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EP1039475	FR	6/25/2003	Address and data transfer circuit
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·			Hans Jurgen Mattaucsh
EP1039475	GB	6/25/2003	Address and data transfer circuit
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EP1039475	T _{IT}	6/25/2003	Hans Jurgen Mattaucsh Address and data transfer circuit
(IT00106456.7)	1 **	(3/24/2000)	rodices and data transfer circuit
•			Hans Jurgen Mattaucsh
10-2000-0015127	KR	3/24/2000	Address and data transfer circuit
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89105448	TW	3/24/2000	Address and data transfer circuit
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			Hans Jurgen Mattaucsh
89101956	TW	2/3/2000	Shared memory
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EP1033722	FR	10/12/2003	Shared memory
(FR00102565.9)	1	(2/7/2000)	·
			Hans Jurgen Mattaucsh
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ED1022500		10/10/0000	Hans Jurgen Mattaucsh
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DE60007010	DE	10/10/2002	Hans Jurgen Mattaucsh
· · · · · · · ·	DE	10/12/2003	Shared memory
(DE20006007010)		(2/7/2000)	
6 974 A69	TIC	2/00/0005	Hans Jurgen Mattaucsh
6,874,068 (09/500,254)	US	3/29/2005	Shared memory
(09/300,234)		(2/8/2000)	Hans Inner Matter to
7,035,875	US	4/25/2006	Hans Jurgen Mattaucsh
(10/296,224)	US	4/25/2006	Method of using multi-media information, system
(10/296,224)		(6/1/2001)	and program recording medium therefor
0000000			Kodama Mei
3555756	JP	(6/6/2000)	Multimedia information utilizing method, recording
(2000-169459)			medium recording multimedia information utilizing
	l		program and multimedia information system
			KODAMA AKRIA
01936836.4	EP	06/01/2001	Method of using multi-media information, system
			and program recording medium therefor
			Kodama Mei
7,098,682	US	8/29/2006	Testing method and tester for semiconductor
(10/497,514)		(12/3/2002)	integrated circuit device comprising high-speed
	ŀ		input/output element
			Sasaki Mamoru
02824261.0	CN	12/3/2002	Testing method and tester for semiconductor
	52.	12/3/2002	integrated circuit device comprising high-speed
			input/output element
	1		mpas carpat cromont
			Sasaki Mamoru
10-0571312	KR	4/10/2006	Testing method and tester for semiconductor
			integrated circuit device comprising high-speed
			input/output element
			Sasaki Mamoru
3446124	JP	(12/14/2001)	Testing method and tester for semiconductor
(2001-370609)	7	(12/17/2001)	integrated circuit device comprising high-speed
(=			input/output element
	1	1	
		j.	

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.
IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Japan on March 31, 2008
ASSIGNOR:
Hiroshima University
By: <u>Joshyuki Skimiyu</u> Name: Toshiyuki Shimizu Title: Executive (Finance) Member of Board of Directors; Authority for Contracts (Signature MUST be attested)
ATTESTATION The undersigned witnessed the signature of Toshiyuki Shimizu to the above Assignment of Patent Rights on behalf of Hiroshima University and makes the following statements:
1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.
2. Toshiyuki Shimizu is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me on April 21, 2008 to execute the above Assignment of Patent Rights on behalf of Hiroshima University.
3. Toshiyuki Shimizu subscribed to the above Assignment of Patent Rights on behalf of Hiroshima University.
declare under penalty of perjury under the laws of the United States of America that the foregoing is true and correct. EXECUTED on March 31, 2008 (date)
Print Name: Eiji Veda